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Form PTO-1449 CU.S. Department of Commerce Attorney D ck t N . 0756-1630 S rial N . 08/799,506 Patent and Trademark Office (Rev. 8-83) Applicant: Shunpei YAMAZAKI t al. INFORMATION DISCLOSURE STATEMENT Filing Date: F bruary 12, 1997 Gr up: 1107 (Use several sheets if necessary) U.S. PATENT DOCUMENTS Examiner **Document Number** Date Name **Class** Subclass Filing Date Initial (if appr priate) 07/02/91 5,028,560 Tsukamoto et al 437 239 06/21/94 5,322,807 Chen et al 08/19/92 437 41TFT Radosevich et al 03/29/94 5,298,436 437 233 5,294,571 03/15/94 Fujishiro et al 437 239 06/30/87 4,675,978 **Swartz** 437 21 mw 5,422,287 06/06/95 So 437 21 5,412,493 05/02/95 Kunii et al 437 41TFT 09/15/92 5,147,826 Liu et al 437 233 12/13/94 437 40 5,372,958 Miyasaka et al 5,663,077 09/02/97 Adachi et al FOREIGN PATENT DOCUMENTS **Document Number** Date Country Class Subclass **Translation** Yes No 3-289140 12/19/91 Japan 437 21 Abstract 53-26584 03/11/78 437 21 Abstract Japan 4-102375 04/03/92 437 X Japan 4£TFT 02 04-251921 09/08/92 Japan Abstract 1 ריוט 03-82121 04/08/91 Japan Abstract 05-206063 08/13/93 Japan **Abstract** OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) S. Wolf, "Silicon Processing for the VLSI Era, Vol. 1: Process Technology" Lattice Press, MW January 1986, pp. 57-58 S. Wolf et al., "Silicon Processing for the VLSI Era, Vol 3: The Submocropher Lattice Press, MU California, October 1985, pp. 502-504 J. Nulman et al., "Rapid Thermal Processing of Thin Gate Die 1200 | 1220 | EEE Electron Device Letters, Vol. 6, No. 5, pp. 205-207, May 1985 United States Defensiv Publicati n T954,009 (Malin et al.), "Meth d for the Thrmal Oxidation of MW Silic n with Added Chlorine," January 4, 1977 2000 **Examiner** Wilczewski Date C nsid red *EXAMINER: Initial if ditation considered, whether or not citation is in conformanc with MPEP 609; Draw line

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